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APPLICANTS										
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ν/λ ** CONTINUING DATA **********************************										
** FOREIGN APPLICATIONS ************************************										
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 08/10/2001										
Foreign Priority claimed	STATE OR	 s⊦	HEETS TO		TAL	INDEPENDE				
35 USC 119 (a-d) conditions met yes no Met after Allowance Verified and Acknowledged Examiner's Signature Initials				COUNTRY RUSSIAN FEDERATION	DRA			AIMS 20-17	CLAIMS 3	
ADDRESS Leo J. Peters LSI LOGIC CORPOI M/S D-106 1551 McCarthy Boul Milpitas , CA 95035										
TITLE Method and apparatus for optimizing the timing of integrated circuits										
						☐ All Fees				
						1.16 Fees (Filing)				
FILING FEE FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT						1.17 Fees (Processing Ext. of time				
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